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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10684350
				Filing Date	10/10/2003
				First Named Inventor	Kevin D. Kissell
(use as many sheets as necessary				Group Art Unit	2195
				Examiner Name	Meng Al T AN
Sheet	1	of	1	Attorney Docket Number	MIPS.0188-01-US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2](if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/EW/		7069421	06-27-2006	Yates Jr., et al.	
/EW/		6877083	04-05-2005	Arimilli et al.	
/EW/		5159686	10-27-1992	Chastain et al.	
/EW/		5867704	02-02-1999	Tanaka et al.	
/EW/		20030014471	01-16-2003	Ohsawa et al.	
/EW/		20030115245	06-19-2003	Kunimasa Fujisawa	

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/EW/		Heuring, Vincent P. and Jordan, Harry F. "Computer Systems and Architecture." Reading, Mass.: Addison Wesley Longman, Inc., ©1997. Pages 35-37.	

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/EW/		7127561	10-24-2006	Hill et al.	
/EW/		6889319	05-03-2005	Rodgers et al.	
/EW/		5812811	09-22-1998	Dubey et al.	

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/EW/		5515538	05-07-1996	Steven R. Kleiman	
/EW/		6061710	05-09-2000	Eickemeyer et al.	
/EW/		6401155	06-04-2002	Saville et al.	
/EW/		6175916	01-16-2001	Ginsberg et al.	
/EW/		5944816	08-31-1999	Dutton et al.	

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/EW/		Patterson et al. "Computer Organization & Design: The Hardware/Software Interface." 1998. Morgan Kaufmann Publishers, Inc. Second Edition. Pages 592-593.	
/EW/		Silberschatz et al. "Operating Systems Concepts." 1994. Addison-Wesley Publishing Company. Fourth Edition. Pages 267-269, 271-272, 275	

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/EW/		6971103	11-29-2005	Hokenek et al.	
/EW/		6993598	01-31-2006	Pafumi et al.	
/EW/		20030079094	04-24-2003	Rajwar et al.	

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/EW/		6697935	02-24-2004	Borkenhagen et al.	
/EW/		5961584	10-05-1999	Mikael Wolf	
/EW/		6675192	01-06-2004	Emer et al.	
/EW/		6687812	02-03-2004	Sachiko Shimada	

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/EW/		20050240936	10-27-2005	Jones et al.	
/EW/		20050251613	11-10-2005	Kevin D. Kissell	
/EW/		20050251839	11-10-2005	Vishin et al.	

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/EW/	AA	KISSELL, KEVIN D. "Microprocessor Forum (MPF) Conference Program Presentation: Architectural Extensions to the MIPS Architecture for High-Performance Embedded Systems." 15 October 2003.	
/EW/	AB	(webpage) Cray MTA-2 Historical Technical Papers, http://www.cray.com/products/programs/mta_2/resources.html. (12March 2005), (formerly http://www.cray.com/products/systems/mta/psdocs.html (7/2/2004)).	
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/EW/	AE	BRIGGS, PRESTON. "Tuning the BLAS for the Tera." Workshop on Multithreaded Execution, Architecture and Compilation. (MTEAC 98), January 1998.	
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/EW/	AG	SMITH, BURTON. "Folklore and Reality in High Performance Computing Slide Presentation." 1995	
/EW/	AH	SMITH, BURTON. "The Quest for General-Purpose Parallel Computing." 1994	
/EW/	AI	ALVERSON, GAIL et al. "Exploiting Heterogeneous Parallelism on a Multithreaded Multiprocessor." 6th ACM International Conference on Supercomputing, Washington DC, July 1992	
/EW/	AJ	CALLAHAN, DAVID. "Recognizing and Parallelizing Bounded Recurrences." Fourth Workshop on Languages and Compilers for Parallel Computing. Pp. 169-184. August 1991.	
/EW/	AK	CALLAHAN, DAVID et al. "Register Allocation via Hierarchical Graph Coloring." ACM SIGPLAN '91 Conference on Programming Language Design and Implementation, June 1991.	
/EW/	AL	ALVERSON, ROBERT. "Integer Division Using Reciprocals." 10th IEEE Symposium on Computer Arithmetic, June 1991.	
/EW/	AM	CALLAHAN, DAVID et al. "Improving Register Allocation for Subscripted Variables." ACM SIGPLAN Conference on Programming Language Design and Implementation, June 1990	
/EW/	AN	ALVERSON, ROBERT et al. "The Tera Computer System." ACM International Conference on Supercomputing, pp. 1-6, June 1990.	
/EW/	AO	SMITH, BURTON. "The End of Architecture." Keynote Address, 17th Annual Symposium on Computer Architecture, Seattle, Washington, May 1990	
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/EW/	AQ	"MIT Alewife Project: Home Page." retrieved from URL: http://catfish.csail.mit.edu/alewife/ on 7/2/2004.	
/EW/	AR	ARVIND and R.A. INNAUCCI. "Two Fundamental Issues in Multiprocessing." In Proc. Of DFVLF-Conf. 1987 on Par.Proc.in Science and Eng., Bonn-Bad Godesberg, W. Germany, June 1987	

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/EW/		20050125795	06-09-2005	Kevin D. Kissell	
/EW/		20050120194	06-02-2005	Kevin D. Kissell	
/EW/		20050125629	06-09-2005	Kevin D. Kissell	

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Complete if Known	
Application Number	10/684350
Filing Date	10/10/03
First Named Inventor	Kevin D. Kissell
Art Unit	2184
Examiner Name	VO, LILIAN
Attorney Docket Number	MIPS.0188-01-US

Sheet	1	of	2

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
/EW/		5659786	08-19-1997	George et al.	
/EW/		6330656B1	12-11-2001	Bealkowski et al.	
/EW/		20030074545A1	04-17-2003	Uhler, G. Michael	
/EW/		6223228B1	04-24-2001	Ryan et al.	
/EW/		5499349A	03-12-1996	Nikhil et al.	
/EW/		2003126416A1	07-03-2003	Marr et al	

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No.[1]	Foreign Patent Document Country Code[3] Number[4] Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T[6]
/EW/	AA	EP1089173A2	04-04-2001	King et al.		
/EW/	AB	EP0917057A2	05-19-1999	Zalewski et al.		
/EW/	AC	EP0725334A1	08-07-1996	Dubey et al.		
/EW/	AD	WO0153935A1	07-26-2001	Marr et al.		

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Sheet	2	of	2	Attorney Docket Number	MIPS.0188-01-US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
/EW/	BA	ENGELSCHALL, R.S., "pth GNU Portable Threads," Pth Manual, Online! 17 Feb 2003, pages 1-31, XP002315713	
/EW/	BB	ISHIHARA ET AL., "A Comparison of Concurrent Programming and Cooperative Multithreading," EURO-PAR 2000 PARALLEL PROCESSING. 6TH INTERNATIONAL EURO-PAR CONFERENCE. PROCEEDINGS (LECTURE NOTES IN COMPUTER SCIENCE VOL. 1900) SPRINGER-VERLAG BERLIN, GERMANY, 1 September 2000, pages 729-738, XP002315714, ISBN: 3-540-67956-1	
/EW/	BC	FREES, W., "'TEILZEITARBEIT IM PROZESSOR," ELECTRONIK, FRANZIS VERLAG GMBH. MUNCHE, DE, Vol. 45, no. 9, 30 April 1996, pages 100-106, XP000595386, ISSN: 0013-5658 (English-language version of the search report or action which indicates the degree of relevance found by the foreign office is appended.)	
/EW/	BD	SCHEIDHAUER, RALF, "Design, Implementierung und Evaluierung einer virtuellen Maschine fur Oz," Online!, December 1998, Dissertation, Saarbrucken, (English-language version of the search report or action which indicates the degree of relevance found by the foreign office is appended.)	
/EW/	BE	MEHL ET AL., "An Abstract Machine for Oz," Research Report RR-95-08, Online!, June 1995, pages 1-23, Kaiserslautern Saarbrucken, ISSN 0946-008	
/EW/	BF	UNGER ET AL., "Utilising Parallel Resources By Speculation," Parallel and Distributed Processing, 1999. PDP '99. Proceedings of the Seventh Euromicro Workshop on Funchal, Portugal 3-5 Feb 1999, Los Alamitos, CA, USA, IEEE Computing Society, 3 Feb 1999, pages 339-343	
/EW/	BG	TSAI ET AL: "The Superthreaded Architecture: Thread Pipelining with Run-Time Data Dependence Checking and Control Speculation," Parallel Architectures and Compiliation Techniques, 1996, Proceedings of the 1996 Conference in Boston, MA, USA, 20-23 Oct 1996, Los Alamitos, CA, USA, IEEE Comput. Soc., 20 Oct 1996, pages 35-46	
/EW/	BH	POPOV, KONSTANTIN: "A Parallel Abstract Machine for the Thread-Based Concurrent Language Oz," Workshop on Parallism and Implementation of Technology for Constraint Logic Programming Languages, September 1997, pages 1-25	
/EW/	BI	UBICOM, INC. A Next Generation Packet Processor for Wireless Networking. April 15, 2003. UBICOM, Inc. Mountain View, CA, USA	
/EW/	BJ	BOLYCHEVSKY ET AL. "Dynamic Scheduling in RISC Architectures." IEEE Proceedings Computers and Digital Techniques, vol. 143, no. 5. 24 September 1996. Pages 309-317.	

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Application Number	10/684350
Filing Date	10/10/03
First Named Inventor	Kevin Kissell
Group Art Unit	
Examiner Name	
Attorney Docket Number	MIPS.0188-01US

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.[1]	U.S. Patent Document Number	Kind Code[2]	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/EW/	AA	5758142		McFarling et al.	05/26/1998	
/EW/	AB	5933627		Parady	08/03/1999	

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Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
/EW/	BA	ALVERSON ET AL., "Tera Hardware-Software Cooperation, " Tera Computer Corporation – Proceedings of the IEEE/ACM SC97 Conference – November 15-21, 1997, San Jose, CA	
/EW/	BB	CARTER ET AL., "Performance and Programming Experience on the Tera MTA, "Tera Computer Corporation – SIAM Conference on Parallel Processing – March 1999.	
/EW/	BC	"Multithreaded Programming Guide," November 1995, Sunsoft Microsystems, Inc., Mountain View, CA, USA	
/EW/	BD	"Hyper-Threading Technology, " Volume 6, Issue 01, Published February 14, 2002 ISSN 1535766X, Intel Technology Journal, USA	

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Group Art Unit	
Examiner Name	
Attorney Docket Number	MIPS.0188-01US

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/EW/	AA	THEO UNGERER, University of Augsburg, Augsburg Germany; BORUT ROBIC, University of Ljubljana, Slovenia; JURIJ SILC, Jozef Stefan Institute, Slovenia. "Multithreaded Program Guide", November 1995. Sunsoft, A Sun Microsystems, Inc. Business, 2550 Garcia Ave., Mountain View, CA, 94043, USA.	
/EW/	AB	THEO UNGERER, University of Augsburg, Augsburg Germany; BORUT ROBIC, University of Ljubljana, Slovenia; JURIJ SILC, Jozef Stefan Institute, Slovenia. "A Survey of Processors with Explicit Multithreading". ACM Computing Surveys, Vol., 35, No. 1, March 2003, pp.29-63.	
/EW/	AC	DAVID FOTLAND; "A Multithreaded Wireless Network Processor with Software I/O". Embedded Processor Forum, June 18, 2003. In-Stat MDR;www.MDRonline.com.	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

Complete if Known	
Application Number	NA
Filing Date	10/10/2003
First Named Inventor	Kevin D. Kissell
Group Art Unit	NA
Examiner Name	NA
Attorney Docket Number	3865.01

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/EW/		UBICOM, INC., A Next-Generation Packet Processor for Wireless Networking, April 15, 2003, Ubicom, Inc., Mountain View, CA, USA	
/EW/		SunSoft, Mutlithreaded Programming Guide, 1995, SunSoft, A Sun Microsystems, Inc. Business, Mountain View, CA USA	

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